

Data Availability Set: Energy/Power Bounds CMOS CS DACs

SND-ID: 2024-334. **Version:** 1. **DOI:** <https://doi.org/10.5878/psy0-mm02>

Ladda ner data

[14] A 5.3GHz 16b 1.75GSps Wideband RF Mixing-DAC Achieving IMD less than -82dBc up to 1.9GHz.pdf (727.29 KB)

[30] A compact dynamic-performance-improved current-steering DAC with random rotation-based binary-weighted selection.pdf (2.34 MB)

[31] A 12b 1.6GSps 40mW DAC in 40nm CMOS with higher than 70dB SFDR over Entire Nyquist Bandwidth.pdf (327.4 KB)

[32] A 12b 2GSps Dual-Rate Hybrid DAC with Pulsed Timing-Error Pre-Distortion and In-Band Noise Cancellation.pdf (4.2 MB)

[33] A 240 mW 16b 3.2 GSps DAC in 65 nm CMOS with less than -80 dBc IM3 up to 600 MHz.pdf (810.91 KB)

[34] A 330 mW 14b 6.8 GSps Dual-Mode RF DAC in 16nm FinFET Achieving -70.8dBc ACPR in a 20MHz Channel at 5.2GHz.pdf (1.21 MB)

[35] A 14 bit 200 MSps DAC with SFDR 78 dBc IM3 83 dBc and NSD 163 dBmHz across the whole Nyquist band.pdf (1.9 MB)

[36] Demystifying and Mitigating Code-Dependent Switching Distortions in Current-Steering DACs.pdf (4.08 MB)

[37] A 12-bit 320-MSampleps Current-Steering CMOS DA Converter in 0.44 mm².pdf (661.94 KB)

[38] A 12b 500MSps DAC with more than 70dB SFDR up to 120MHz in 0.18um CMOS.pdf (801.56 KB)

[39] A 12 bit 1GSps dual-rate hybrid DAC with an 8 GSps unrolled pipeline delta-sigma modulator.pdf (2.99 MB)

[40] A 16b 12GSps Single-Dual-Rate DAC with Successive Bandpass Delta-Sigma Modulator.pdf (906.99 KB)

[41] A 16-bit 10Gsps current steering RF DAC in 65nm CMOS achieving 65dBc ACLR multi-carrier performance at 4.5GHz Fout.pdf (301.92 KB)

[9] A 0.07mm² 162mW DAC Achieving 65 dBc SFDR and 70 dBc IM3 at 10 GSps With Output Impedance Compensation and Concentric Parallelogram Routing.pdf (3.03 MB)

Data Points.xlsx (26.2 KB)

Ladda ner alla filer

2024-334-1.zip (~24.22 MB)

Citering

Morales Chacón, O. A. (2021) Data Availability Set: Energy/Power Bounds CMOS CS DACs (Version 1) [Dataset]. Linköpings universitet. Tillgänglig via: <https://doi.org/10.5878/psy0-mm02>

Skapare/primärforskare

Oscar A. Morales Chacón - Linköpings universitet

Forskningshuvudman

[Linköpings universitet](#)

Beskrivning

Se engelsk beskrivning för info.

Datasetet har ursprungligen publicerats i DiVA och flyttades över till SND 2024.

Data innefattar personuppgifter

Nej

Språk

[Engelska](#)

Dataformat / datastruktur

[Text](#)

Identifierare

URN: [urn:nbn:se:liu:diva-178137](https://nbn-resolving.org/urn:nbn:se:liu:diva-178137)

Forskningsområde

[Annan elektroteknik och elektronik](#) (Standard för svensk indelning av forskningsämnen 2011)

Nyckelord

[Digital-analog-omvandlare](#), [Energiförbrukning \(energiteknik\)](#), [Omvandlare](#)

Publikationer

Morales Chacón, O., Wikner, J.J., Svensson, C. et al. Analysis of energy consumption bounds in CMOS current-steering digital-to-analog converters. Analog Integr Circ Sig Process 111, 339–351 (2022).

<https://doi.org/10.1007/s10470-022-02013-2>

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DOI: <https://doi.org/10.1007/s10470-022-02013-2>

Tillgänglighetsnivå

Åtkomst till data via SND

Data är fritt tillgängliga

Användning av data

[Att tänka på vid användning av data som delas via SND](#)

Versioner

Version 1. 2021-08-04

Ladda ner metadata

[DataCite](#)

[DDI 2.5](#)

[DDI 3.3](#)

[DCAT-AP-SE 2.0](#)

[JSON-LD](#)

[PDF](#)

[Citation \(CSL\)](#)

[Filöversikt \(CSV\)](#)

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